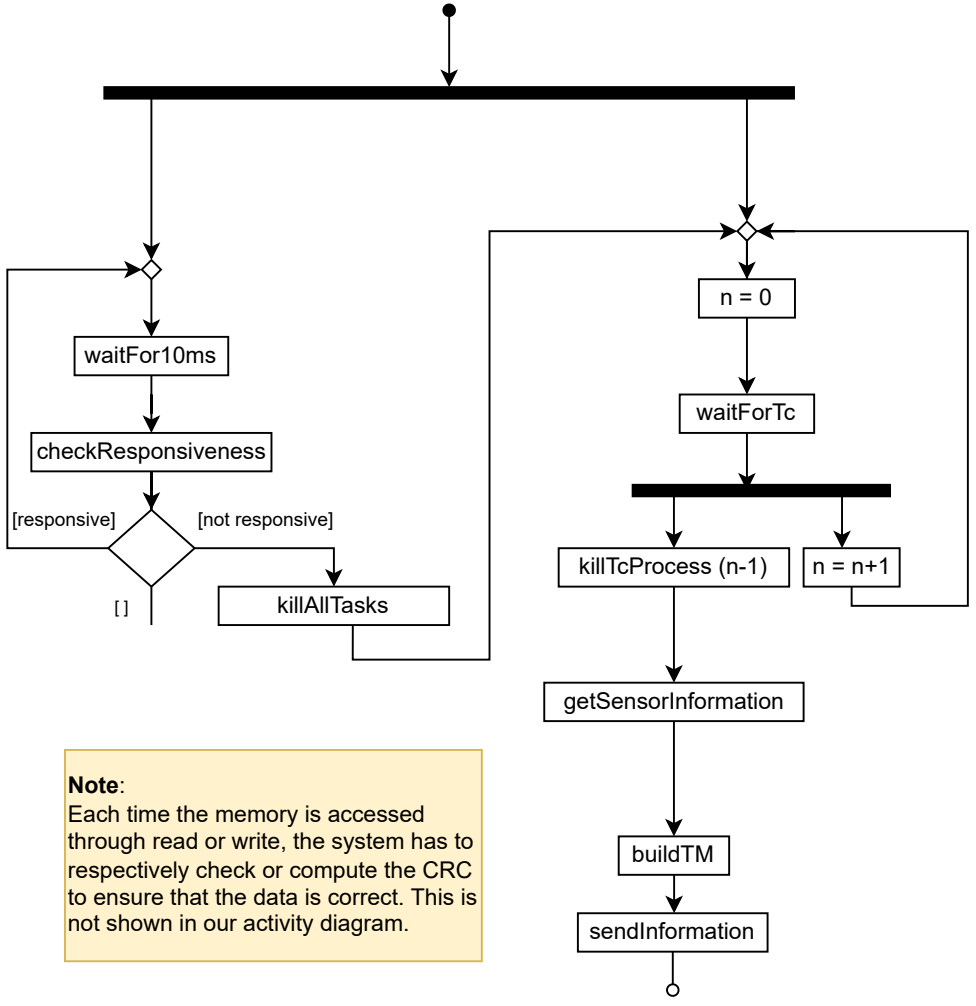

Note:
Each time the memory is accessed through read or write, the system has to respectively check or compute the CRC to ensure that the data is correct. This is not shown in our activity diagram.